



Greg Kisor
Vice President, Portfolio Architect

Greg Kisor is vice president, portfolio architect at Intellectual Ventures where he focuses on a variety of projects relating to intellectual property and invention.

Prior to joining Intellectual Ventures, Mr. Kisor spent 10 years at Intel Corporation where he held numerous jobs including Principal Engineer & Chief Patent Technologist. As Chief Patent Technologist he was responsible for IP strategy, portfolio developments and license negotiations.

Prior to his role as Patent Technologist at Intel Mr. Kisor was the Lead architect for many of Intel's products, including Video & Data Conferencing, Java Implementations, and Digital Video Strategy. Mr. Kisor was Chairman of the United States JPEG Committee and has held many high positions in International Standards, including Head of delegation to ISO/IEC JTC1 SC29 Image Compression Standards. Mr. Kisor also has held engineering and lead architect positions at National Semiconductor and IBM. Mr. Kisor currently holds 15 patents with many more pending and received his B.S.E.E. from the Brigham Young University in 1988.